

09/92/1518

438

Class	Subclass
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**ISSUE CLASSIFICATION**

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

SCANNED 796①

## Q.A.

APPLICATION NO.  
09/92151

**CONT/PRIOR**

**CLASS**

SUBCLASS

ART UNIT

EXAMINER

John Moore

## APPLICANTS:

**TITLE**

Method of forming integrated circuitry, method of forming memory circuitry, and method of forming non-volatile random access memory circuitry

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]☐ Continued on Issue Slip Inside File Jacket

## TERMINAL DISCLAIMER

☐ The term of this patent subsequent to \_\_\_\_\_ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. \_\_\_\_\_

☐ The terminal \_\_\_\_ months of this patent have been disclaimed.

## DRAWINGS

Sheets Drwg.

**Figs. Drwg.**

Print Fig.

**CLAIMS ALLOWED****Total Claims**

Print Claim for O.G.

**NOTICE OF ALLOWANCE MAILED**

**ISSUE FEE**

Amount Due

Date Paid

**ISSUE BATCH NUMBER**

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